IN THE CLAIMS:

Please amend claims 1 and 14-17, and add new claims 18-21 as follows:

1. (Currently Amended) A liquid crystal display device including a data driver and a gate driver, comprising:

a liquid crystal display panel having two opposing edges; and

a substrate on which said liquid crystal display panel, and the data driver, and the gate driver are integrally formed,

the data driver on a single edge of the two opposing edges of the liquid crystal display panel being divided into a plurality of blocks so as to divide the liquid crystal display panel into sections arranged side by side, which simultaneously supply the liquid crystal display panel with display signals respectively supplied thereto;

wherein each of said blocks includes a plurality of signal lines that are connected to a plurality of data bus lines via analog switches, a number of said data bus lines being larger than a number of said signal lines, said display signals being supplied from the signal lines of each block to the data bus lines simultaneously, and said blocks are arranged adjacent to each other along [a] the single edge of the liquid crystal display panel.

2. (Original) The liquid crystal display device as claimed in claim 1, wherein each of the blocks comprises:

a shift register;

signal lines connected to the signal lines and the liquid crystal display panel; and

analog switches provided in the data bus lines and controlled by an output signal of the shift register.

3. (Original) The liquid crystal display device as claimed in claim 1, further comprising a driver device which receives display data externally supplied and outputs the display signals derived therefrom to the blocks of the data driver.

4. (Original) The liquid crystal display device as claimed in claim 1, further comprising a plurality of driver devices which are respectively associated with a plurality of ones of the blocks, each of the plurality of driver devices receiving display data externally supplied and outputting the display signals derived therefrom to associated blocks of the data driver.

5. (Original) The liquid crystal display device as claimed in claim 4, wherein the display signal lines of the associated blocks have parts extending from one of the plurality of driver devices through a space located between the associated blocks.

6. (Original) The liquid crystal display device as claimed in claim 1, further comprising a substrate on which said liquid crystal display panel, said data driver, and said gate driver are integrally formed.

7. (Original) The liquid crystal display device as claimed in claim 1, wherein said data driver comprises polysilicon transistors.

8. (Original) The liquid crystal display device as claimed in claim 3, further comprising a display signal supply device which outputs the display data to the driver device.

9. (Original) The liquid crystal display device as claimed in claim 8, wherein the display signal display device is formed on the liquid crystal display panel.

10. (Original) The liquid crystal display device as claimed in claim 4, further comprising a display signal supply device which outputs the display data to the plurality of driver devices.

11. (Original) The liquid crystal display device as claimed in claim 1, wherein each of the plurality of blocks supplies the liquid crystal display panel with a given number of display signals at once.

12. (Original) The liquid crystal display device as claimed in claim 3, wherein said driver device comprises a shift register which outputs a shift signal, first latch circuits which latches the display data in response to the shift signal, and second latch circuits which latches the display data from the first latch circuits in response to a latch enable signal externally supplied.

13. (Original) The liquid crystal display device as claimed in claim 12, further comprising digital-to-analog converters which convert the display data from the second latch circuits into analog signals.

14. (Currently Amended) A liquid crystal display device including a data driver and a gate driver, comprising:

a liquid crystal display panel <u>having two opposing edges</u>; and

groups of signal lines for carrying display signals, said signal lines within each of said groups being adjacent to each other along a single edge <u>of said two opposing edges</u> of said liquid crystal display panel,

the data driver being divided into a plurality of adjacently arranged blocks from which said groups of signal lines extend over corresponding partial areas of the liquid crystal display device so that each of said groups of signal lines is associated with a respective one of said blocks of the data driver, wherein said signal lines in each of said blocks are connected to a plurality of data bus lines via analog switches, a number of said data bus lines is larger than a number of said signal lines, and the display signals are supplied from the signal lines of each block to the data bus lines simultaneously.

15. (Currently Amended) A liquid crystal display device including a data driver and a gate driver, comprising:

a liquid crystal display panel[,] <u>having two opposing edges;</u> and

signal lines extending from the data driver and carrying display signals,

the data driver and the signal lines being divided into a plurality of blocks so that said divided signal lines extending from one of said plurality of blocks extend over a corresponding divided area of the liquid crystal display device,

wherein said plurality of blocks are adjacent to each other along a single edge <u>of said two opposing edges</u> of said liquid crystal display panel, said divided signal lines in each of said plurality of blocks are connected to a plurality of data bus lines via analog switches, a number of said data bus lines being larger than a number of said signal lines, and

display signals being supplied from said signal lines of each of said blocks to said data bus lines simultaneously.

16. (Currently Amended) A liquid crystal display device including a data driver and a gate driver, comprising:

a liquid crystal display panel <u>having two opposing edges</u>; and

a substrate on which said liquid crystal display panel, the data driver, and the gate driver are integrally formed,

wherein the data driver is divided into a plurality of blocks arranged side by side along a single edge <u>of said two opposing edges</u> of the liquid crystal display panel, and each of said blocks has a plurality of signal lines that extend into the liquid crystal display device and are connected to a plurality of data bus lines via analog switches, a number of said data bus lines being larger than a number of said signal lines, and display signals being supplied from said signal lines of each block to said data bus lines simultaneously.

17. (Currently Amended) The liquid crystal display ~~panel~~ <u>device</u> as claimed in claim 16, wherein said data driver comprises polysilicon transistors.

18. (New) The liquid crystal display panel as claimed in claim 1, wherein said each of said blocks is arranged adjacent to a block of at least one of an immediately preceding block and an immediately following block.

19. (New) The liquid crystal display panel as claimed in claim 14, wherein said each of said blocks is arranged adjacent to a block of at least one of an immediately preceding block and an immediately following block.

20. (New) The liquid crystal display panel as claimed in claim 15, wherein said each of said blocks is arranged adjacent to a block of at least one of an immediately preceding block and an immediately following block.

21. (New) The liquid crystal display panel as claimed in claim 16, wherein said each of said blocks is arranged adjacent to a block of at least one of an immediately preceding block and an immediately following block.